

Table with columns: ALLE ADRESSEN HEX, PHYSIKALISCHE ZUORDNUNG, BUSSIZE, FB_CS, MMU PAGE SIZE = 1MB, CACHE MODE, and bit positions 31-0. Rows include memory regions like DDRAM, VIDEO RAM, PCI MEMORY, 8MB FLASH, ACP I/O, SRAM, and MUBAR with associated hardware details and bit field descriptions.